

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Donald E. Steiss, et al.
SERIAL NO: 10/773,385
FILING DATE: February 5, 2004
TITLE: Thread-Aware Instruction Fetching In A Multithreaded Embedded Processor
EXAMINER: Eric Coleman
ART UNIT: 2183
ATTY. DKT. NO.: 22347-08564 (8422)

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Amendment and Response Under 37 CFR § 1.111

SIR:

The following amendments and remarks are in response to the Non-Final Office Action dated January 11, 2007.